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Application Number **SUBMIT**

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10/632,079

Processor with a split stack 

Application Data  Transaction History  Image File Wrapper  Continuity Data  Foreign Priority  Published Documents  Publication Dates  Address & Attorney/Agent

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07-31-2003	<b>Application Data Sheet</b>	3	<input type="checkbox"/>		
07-31-2003	<u>Information Disclosure Statement (IDS) Filed</u>	2	<input type="checkbox"/>		
07-31-2003	<b>Fee Worksheet (PTO-875)</b>	1	<input type="checkbox"/>		
07-31-2003	<u>Fee Worksheet (PTO-875)</u>	1	<input type="checkbox"/>		

## Application Data Sheet

### Application Information

Application Type:: Regular  
Subject Matter:: Utility  
Title:: A Processor With A Split Stack  
Attorney Docket Number:: TI-35425 (1962-05404)  
Request for Early Publication?:: No  
Request for Non-Publication?:: No  
Suggested Drawing Figure:: 2  
Total Drawing Sheets:: 6  
Small Entity?:: No

### Applicant Information

Applicant Authority type::	Inventor
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Applicant Authority type:: Inventor  
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Given Name:: Serge  
Family Name:: LASSERRE

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Applicant Authority type:: Inventor  
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#### **Correspondence Information**

Correspondence Customer Number:: 23494

#### **Representative Information**

Representative Customer Number:: 23494

#### **DOMESTIC PRIORITY INFORMATION**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Provisional of	60/400,391	07/31/02

**FOREIGN PRIORITY INFORMATION**

Country::	Application number::	Filing Date::	Priority Claimed::
EPO	03291908.6	July 30, 2003	YES

**Assignee Information**

Assignee name:: Texas Instruments Incorporated  
Street of mailing address:: P.O. Box 655474, MS 3999  
City of mailing address:: Dallas  
State or Province of  
mailing address:: TX  
Country of mailing  
address:: US  
Postal or Zip Code of  
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